

PATENT APPLICATION

Sheet 1 of 1

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
	10017494-1	10/7/8,137	
	APPLICANT		
	Neal W. Meyer et al.		
	FILING DATE	GROUP	
	Herewith 11/20/03	2822	

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
TMT	1A	5,636,441	06-1997	Meyer et al.	
	1B	5,682,188	10-1997	Meyer et al.	
	1C	5,851,902	12-1998	Sakai	
	1D	6,248,649	06-2001	Henley et al.	
TMT	1E	6,315,393	11-2001	Van Nice et al.	
	1F				
	1G				
	1H				
	1I				
	1J				
	1K				

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

TMT	1Q	Simonton, Robert, "SOI Wafer Technology for CMOS ICs", 2002, Simonton Associates, pp. 1-11.
TMT	1R	"Soitec Buys Selected Assets of Picogiga", April 1, 2003, www.compoundsemiconductor.net/articles/news/7/4/1/1/ , pp. 1-2.
TMT	1S	SiGen Research and Development Programs, www.sigen.com/research.htm , pp. 1-3.

EXAMINER

DATE CONSIDERED

T. M. Thomas

12-02-2004